

# APPROVAL SHEET

## MULTILAYER CERAMIC CAPACITORS

Ultra-small Series (6.3V to 50V)

0201 Size

NP0, X7R, X5R Dielectrics

RoHS Compliance

\*Contents in this sheet are subject to change without prior notice.

## 1. INTRODUCTION

MLCC consists of a conducting material and electrodes. To manufacture a chip-type SMT and achieve miniaturization, high density and high efficiency, ceramic condensers are used.

0201 MLCC is performed by high precision technology achieve high capacitance in unit size and ensure the stability and reliability of products.

## 2. FEATURES

- b. High capacitance in unit size.
- c. High precision dimensional tolerances.
- d. Suitable used in high-accuracy automatic mounting machine.

## 3. APPLICATIONS

- a. Miniature microwave module.
- b. Portable equipments (ex. Mobile phone, PDA).
- c. High frequency circuits.

## 4. HOW TO ORDER

<u>0201</u>	<u>B</u>	<u>102</u>	<u>K</u>	<u>250</u>	<u>C</u>	<u>I</u>
<u>Size</u>	<u>Dielectric</u>	<u>Capacitance</u>	<u>Tolerance</u>	<u>Rated voltage</u>	<u>Termination</u>	<u>Packaging</u>
Inch (mm) <b>0201</b> (0603)	<b>N</b> =NP0 (C0G) <b>B</b> =X7R <b>X</b> =X5R	Two significant digits followed by no. of zeros. And R is in place of decimal point.  eg.: 0R5=0.5pF 1R0=1.0pF 102=10x10 <sup>2</sup> =1000pF	<b>B</b> =±0.1pF <b>C</b> =±0.25pF <b>D</b> =±0.5pF <b>F</b> =±1% <b>G</b> =±2% <b>J</b> =±5% <b>K</b> =±10% <b>M</b> =±20% <b>Z</b> =-20/+80%	Two significant digits followed by no. of zeros. And R is in place of decimal point.  <b>6R3</b> =6.3 VDC <b>100</b> =10 VDC <b>160</b> =16 VDC <b>250</b> =25 VDC <b>500</b> =50 VDC	<b>L</b> =Ag/Ni/Sn (for NP0 dielectric) <b>C</b> =Cu/Ni/Sn (for X7R, X5R dielectric)	<b>T</b> =7" reeled

## 5. EXTERNAL DIMENSIONS

Size Inch (mm)	L (mm)	W (mm)	T (mm)/Symbol	M <sub>B</sub> (mm)
0201 (0603)	0.60±0.03	0.30±0.03	0.30±0.03 L	0.15±0.05

\* Reflow soldering only.

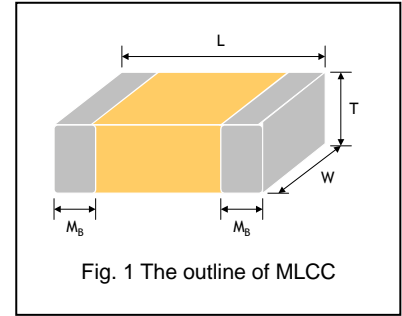


Fig. 1 The outline of MLCC

## 6. GENERAL ELECTRICAL DATA

Size	0201		
Dielectric	NP0	X7R	X5R
Capacitance*	0.3pF to 100pF	100pF to 10nF	100pF to 0.47μF
Capacitance tolerance**	Cap≤5pF: B (±0.1pF), C (±0.25pF) 5pF<Cap<10pF: C (±0.25pF),D(±0.5pF) Cap≥10pF: F (±1%), G (±2%), J (±5%), K (±10%)	J (±5%), K (±10%), M (±20%)	J (±5%),K (±10%), M (±20%)
Rated voltage (WVDC)	16V, 25V, 50V	6.3V, 10V, 16V, 25V, 50V	6.3V, 10V, 16V, 25V, 50V
Tan δ / Q*	Cap<30pF, Q≥400+20C Cap≥30pF, Q≥1000	Note 1	
Insulation resistance at Ur	≥10GΩ	≥10GΩ or RxC≥500ΩxF whichever is less	
Operating temperature	-55 to +125℃		-55 to +85℃
Capacitance change	±30ppm	±15%	
Termination	Ni/Sn (lead-free termination)		

\* Measured at 30~70% related humidity.

NP0: Apply 1.0±0.2Vrms, 1.0MHz±10% at the condition of 25℃ ambient temperature.

X7R, X5R: Apply 1.0±0.2Vrms, 1.0kHz±10%(0201/6.3V, Cap≥224 : 0.5±0.2Vrms, 1.0kHz±10%) at the condition of 25℃ ambient temperature.

\*\* Preconditioning for Class II MLCC: Perform a heat treatment at 150±10℃ for 1 hour, then leave in a mbient condition for 24±2 hours before measurement.

Note 1:

X7R/X5R

Rated vol.	D.F.	Exception of D.F.	
≥50V	≤3%	---	
25V	≤3.5%	≤5%	0201≥0.01uF
16V	≤3.5%	≤5%	0201≥0.01uF
10V	≤5%	≤15%	0201≥0.1uF
6.3V	≤10%	≤15%	0201≥0.1uF

## 7. CAPACITANCE RANGE

SIZE		0201		
DIELECTRIC		NP0		
RATED VOLTAGE (VDC)		16	25	50
Capacitance	0.3pF (0R3)	L^	L^	L^
	0.4pF (0R4)	L^	L^	L^
	0.5pF (0R5)	L^	L^	L^
	1.0pF (1R0)	L^	L^	L^
	1.2pF (1R2)	L^	L^	L^
	1.5pF (1R5)	L^	L^	L^
	1.8pF (1R8)	L^	L^	L^
	2.2pF (2R2)	L^	L^	L^
	2.7pF (2R7)	L^	L^	L^
	3.0pF (3R0)	L^	L^	L^
	3.3pF (3R3)	L^	L^	L^
	3.9pF (3R9)	L^	L^	L^
	4.0pF (4R0)	L^	L^	L^
	4.7pF (4R7)	L^	L^	L^
	5.6pF (5R6)	L^	L^	L^
	6.8pF (6R8)	L^	L^	L^
	8.2pF (8R2)	L^	L^	L^
	10pF (100)	L^	L^	L^
	12pF (120)	L^	L^	L^
	15pF (150)	L^	L^	L^
	18pF (180)	L^	L^	L^
	22pF (220)	L^	L^	L^
	27pF (270)	L^	L^	L^
	33pF (330)	L^	L^	L^
	39pF (390)	L^	L^	L^
	47pF (470)	L^	L^	L^
	56pF (560)	L^	L^	L^
	68pF (680)	L^	L^	L^
	82pF (820)	L^	L^	L^
	100pF (101)	L^	L^	L^

SIZE		0201									
DIELECTRIC		X7R					X5R				
RATED VOLTAGE		6.3	10	16	25	50	6.3	10	16	25	50
Capacitance	100pF (101)			L	L	L			L	L	L
	120pF (121)			L	L	L			L	L	L
	150pF (151)			L	L	L			L	L	L
	180pF (181)			L	L	L			L	L	L
	220pF (221)			L	L	L			L	L	L
	270pF (271)			L	L	L			L	L	L
	330pF (331)			L	L	L			L	L	L
	390pF (391)			L	L	L			L	L	L
	470pF (471)			L	L	L			L	L	L
	560pF (561)			L	L	L			L	L	L
	680pF (681)			L	L	L			L	L	L
	820pF (821)			L	L	L			L	L	L
	1,000pF (102)	L	L	L	L	L		L	L	L	L
	1,500pF (152)	L	L	L				L	L		
	2,200pF (222)	L	L	L				L	L		
	3,300pF (332)	L	L	L				L	L		
	4,700pF (472)	L	L	L				L	L		
	6,800pF (682)	L	L					L			
	8,200pF (822)	L	L					L			
	0.010μF (103)	L	L	L			L	L			
	0.015μF (153)						L	L			
	0.022μF (223)						L	L			
	0.033μF (333)						L	L			
	0.047μF (473)						L	L			
	0.068μF (683)						L	L			
	0.082μF (823)						L	L			
	0.10μF (104)						L	L			
	0.22μF (224)						L				
	0.47μF (474)						L				

1. The letter in cell is expressed the symbol of product thickness.
2. The letter in cell with "^^" mark is expressed product with Ag/Ni/Sn terminations.

## 8. PACKAGING DIMENSION AND QUANTITY

Size	Thickness (mm)/Symbol		Paper tape	
			7" reel	13" reel
0201 (0603)	0.30±0.03	L	15K	70k

Unit: pieces

## 9. RELIABILITY TEST CONDITIONS AND REQUIREMENTS

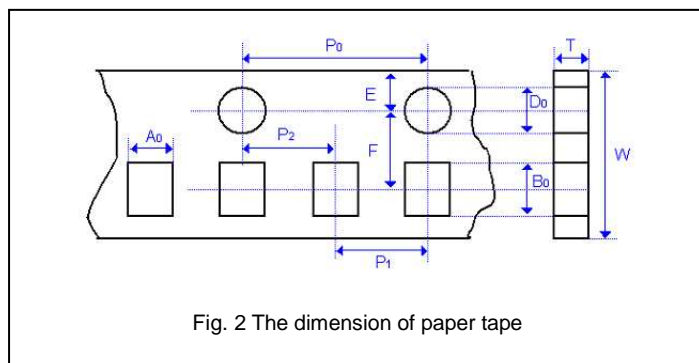
No.	Item	Test Condition	Requirements																								
1.	Visual and Mechanical	---	* No remarkable defect. * Dimensions to conform to individual specification sheet.																								
2.	Capacitance	Class I: NP0	Shall not exceed the limits given in the detailed spec.																								
3.	Q/ D.F. (Dissipation Factor)	Cap≤1000pF, 1.0±0.2Vrms, 1MHz±10%	NP0: Cap≥30pF, Q≥1000; Cap<30pF, Q≥400+20C X7R, X5R: <table><tr><th>Rated vol.</th><th>D.F.</th><th colspan="2">Exception of D.F.</th></tr><tr><td>≥50V</td><td>≤3%</td><td>---</td><td></td></tr><tr><td>25V</td><td>≤3.5%</td><td>≤5%</td><td>0201≥0.01uF</td></tr><tr><td>16V</td><td>≤3.5%</td><td>≤5%</td><td>0201≥0.01uF</td></tr><tr><td>10V</td><td>≤5%</td><td>≤15%</td><td>0201≥0.1uF</td></tr><tr><td>6.3V</td><td>≤10%</td><td>≤15%</td><td>0201≥0.1uF</td></tr></table>	Rated vol.	D.F.	Exception of D.F.		≥50V	≤3%	---		25V	≤3.5%	≤5%	0201≥0.01uF	16V	≤3.5%	≤5%	0201≥0.01uF	10V	≤5%	≤15%	0201≥0.1uF	6.3V	≤10%	≤15%	0201≥0.1uF
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Class II: X7R, X5R																											
1.0±0.2Vrms, 1kHz±10%**																											
**0.5±0.2Vrms, 1.0kHz±10% : 0201 ≥0.22 uF(6.3V)																											
4a.	Dielectric Strength	* To apply voltage (≤100V) 250%. * Duration: 1 to 5 sec. * Charge and discharge current less than 50mA.	No evidence of damage or flash over during test.																								
5.	Insulation Resistance	To apply rated voltage for max. 120 sec.	≥10GΩ or RxC≥500Ω-F whichever is smaller. Class II (X5R, X6S, X7R, Y5V) <table><tr><th>Rated voltage</th><th>Insulation resistance</th></tr><tr><td>6.3V; 10V:0201≥47nF</td><td>≥100 Ω-F</td></tr></table>	Rated voltage	Insulation resistance	6.3V; 10V:0201≥47nF	≥100 Ω-F																				
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6.	Temperature Coefficient	With no electrical load.	<table><tr><th>T.C.</th><th>Capacitance Change</th></tr><tr><td>NP0 (C0G)</td><td>Within ±30ppm/°C</td></tr><tr><td>X7R</td><td>Within ±15%</td></tr><tr><td>X5R</td><td>Within ±15%</td></tr></table>	T.C.	Capacitance Change	NP0 (C0G)	Within ±30ppm/°C	X7R	Within ±15%	X5R	Within ±15%																
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		<table><tr><th>T.C.</th><th>Operating Temp</th></tr><tr><td>NP0 (C0G)</td><td>-55~125°C at 25°C</td></tr><tr><td>X7R</td><td>-55~125°C at 25°C</td></tr><tr><td>X5R</td><td>-55~85°C at 25°C</td></tr></table>		T.C.	Operating Temp	NP0 (C0G)	-55~125°C at 25°C	X7R	-55~125°C at 25°C	X5R	-55~85°C at 25°C																
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7.	Adhesive Strength of Termination	* Pressurizing force : 2N * Test time: 10±1 sec.	* No remarkable damage or removal of the terminations.																								
8.	Vibration Resistance	* Vibration frequency: 10~55 Hz/min. * Total amplitude: 1.5mm * Test time: 6 hrs. (Two hrs each in three mutually perpendicular directions.)	* No remarkable damage. * Cap change and Q/D.F.: To meet initial spec.																								
9.	Solderability	* Solder temperature: 235±5°C * Dipping time: 2±0.5 sec.	95% min. coverage of all metalized area.																								
10.	Bending Test	* The middle part of substrate shall be pressurized by means of the pressurizing rod at a rate of about 1 mm per second until the deflection becomes 1 mm and then the pressure shall be maintained for 5±1 sec.	* No remarkable damage.																								
		* Measurement to be made after keeping at room temp. for 24±2 hrs.	* Cap change: NP0: within ±5.0% or ±0.5pF whichever is larger. X7R, X5R: within ±12.5% Y5V: within ±30% (This capacitance change means the change of capacitance under specified flexure of substrate from the capacitance measured before the test.)																								

No.	Item	Test Condition	Requirements																								
11.	Resistance to Soldering Heat	<ul style="list-style-type: none"><li>* Solder temperature: 260±5℃</li><li>* Dipping time: 10±1 sec</li><li>* Preheating: 120 to 150℃ for 1 minute before immerse the capacitor in a eutectic solder.</li><li>* Before initial measurement (Class II only): Perform 150+0/-10℃ for 1 hr and then set for 48±4 hrs at room temp.</li><li>* Measurement to be made after keeping at room temp. for 24±2 hrs. (Class I) or 48±4 hrs. (Class II).</li></ul>	<ul style="list-style-type: none"><li>* No remarkable damage.</li><li>* Cap change:<ul style="list-style-type: none"><li>NP0: within ±2.5% or ±0.25pF whichever is larger.</li><li>X7R, X5R: within ±7.5%</li><li>Y5V: within ±20%</li></ul></li><li>* Q/D.F., I.R. and dielectric strength: To meet initial requirements.</li><li>* 25% max. leaching on each edge.</li></ul>																								
12.	Temperature Cycle	<ul style="list-style-type: none"><li>* Conduct the five cycles according to the temperatures and time.</li></ul> <table><tr><th>Step</th><th>Temp. (℃)</th><th>Time (min.)</th></tr><tr><td>1</td><td>Min. operating temp. +0/-3</td><td>30±3</td></tr><tr><td>2</td><td>Room temp.</td><td>2~3</td></tr><tr><td>3</td><td>Max. operating temp. +3/-0</td><td>30±3</td></tr><tr><td>4</td><td>Room temp.</td><td>2~3</td></tr></table> <ul style="list-style-type: none"><li>* Before initial measurement (Class II only): Perform 150+0/-10℃ for 1 hr and then set for 48±4 hrs at room temp.</li><li>* Measurement to be made after keeping at room temp. for 24±2 hrs. (Class I) or 48±4 hrs. (Class II).</li></ul>	Step	Temp. (℃)	Time (min.)	1	Min. operating temp. +0/-3	30±3	2	Room temp.	2~3	3	Max. operating temp. +3/-0	30±3	4	Room temp.	2~3	<ul style="list-style-type: none"><li>* No remarkable damage.</li><li>* Cap change:<ul style="list-style-type: none"><li>NP0: within ±2.5% or ±0.25pF whichever is larger.</li><li>X7R, X5R: within ±7.5%</li><li>Y5V: within ±20%</li></ul></li><li>* Q/D.F., I.R. and dielectric strength: To meet initial requirements.</li></ul>									
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3	Max. operating temp. +3/-0	30±3																									
4	Room temp.	2~3																									
13.	Humidity (Steady State)	<ul style="list-style-type: none"><li>* Test temp.: 40±2℃</li><li>* Humidity: 90~95% RH</li><li>* Test time: 500+24/-0hrs.</li><li>* Measurement to be made after keeping at room temp. for 24±2 hrs. (Class I) or 48±4 hrs. (Class II).</li></ul>	<ul style="list-style-type: none"><li>* No remarkable damage.</li><li>* Cap change: NP0: within ±5.0% or ±0.5pF whichever is larger.<ul style="list-style-type: none"><li>X7R, X5R: ≥10V, within ±12.5%, 10V ≥0.1μF, within ±25%; 6.3V, within ±25%</li><li>Y5V: ≥10V, within ±30% 6.3V, within +30/-40%</li></ul></li><li>* Q/D.F. value:<ul style="list-style-type: none"><li>NP0: Cap≥30pF, Q≥350; 10pF≤Cap&lt;30pF, Q≥275+2.5C Cap&lt;10pF; Q≥200+10C</li><li>X7R, X5R:</li></ul></li></ul> <table><tr><th>Rated vol.</th><th>D.F.</th><th colspan="2">Exception of D.F.</th></tr><tr><td>≥50V</td><td>≤6%</td><td>---</td><td></td></tr><tr><td>25V</td><td>≤5%</td><td>≤10%</td><td>0201≥0.01uF</td></tr><tr><td>16V</td><td>≤5%</td><td>≤15%</td><td>0201≥0.01uF</td></tr><tr><td>10V</td><td>≤7.5%</td><td>≤20%</td><td>0201≥0.1uF</td></tr><tr><td>6.3V</td><td>≤15%</td><td>≤30%</td><td>0201≥0.1uF</td></tr></table> <ul style="list-style-type: none"><li>* I.R.: ≥10V, ≥1GΩ or RxC≥50Ω-F whichever is smaller. 6.3V: 10V:0201≥47nF, RxC≥10Ω-F</li></ul>	Rated vol.	D.F.	Exception of D.F.		≥50V	≤6%	---		25V	≤5%	≤10%	0201≥0.01uF	16V	≤5%	≤15%	0201≥0.01uF	10V	≤7.5%	≤20%	0201≥0.1uF	6.3V	≤15%	≤30%	0201≥0.1uF
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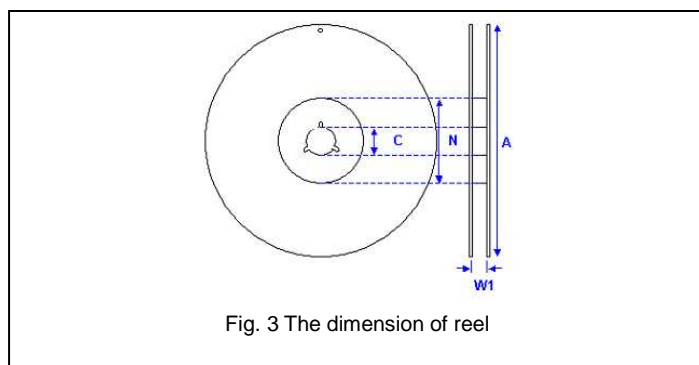
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14.	Humidity Load (Damp Heat)	<ul style="list-style-type: none"><li>* Test temp.: 40±2℃</li><li>Humidity: 90~95%RH</li><li>* Test time: 500+24/-0 hrs.</li><li>* To apply voltage : rated voltage.</li></ul> Measurement to be made after keeping at room temp. for 24±2 hrs. (Class I) or 48±4 hrs. (Class II).	<ul style="list-style-type: none"><li>* No remarkable damage.</li><li>* Cap change: NP0: within ±7.5% or ±0.75pF whichever is larger. X7R, X5R: ≥10V, within ±12.5%, 10V ≥0.1μF, within ±25%; 6.3V, within ±25% Y5V: ≥10V, within ±30% 6.3V, within +30/-40%</li><li>* Q/D.F. value: NP0: Cap≥30pF, Q≥200; Cap&lt;30pF, Q≥100+10/3C X7R, X5R:</li></ul> <table><tr><th>Rated vol.</th><th>D.F.</th><th colspan="2">Exception of D.F.</th></tr><tr><td>≥50V</td><td>≤6%</td><td>---</td><td></td></tr><tr><td>25V</td><td>≤5%</td><td>≤10%</td><td>0201≥0.01uF</td></tr><tr><td>16V</td><td>≤5%</td><td>≤15%</td><td>0201≥0.01uF</td></tr><tr><td>10V</td><td>≤7.5%</td><td>≤20%</td><td>0201≥0.1uF</td></tr><tr><td>6.3V</td><td>≤15%</td><td>≤30%</td><td>0201≥0.1uF</td></tr></table> <ul style="list-style-type: none"><li>* I.R.: ≥10V,500MΩ or RxC≥25Ω-F whichever is smaller. 6.3V; 10V:0201≥47nF, RxC≥5Ω-F</li></ul>	Rated vol.	D.F.	Exception of D.F.		≥50V	≤6%	---		25V	≤5%	≤10%	0201≥0.01uF	16V	≤5%	≤15%	0201≥0.01uF	10V	≤7.5%	≤20%	0201≥0.1uF	6.3V	≤15%	≤30%	0201≥0.1uF
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6.3V	≤15%	≤30%	0201≥0.1uF																								
15.	High Temperature Load (Endurance)	<ul style="list-style-type: none"><li>* Test temp.: NP0, X7R: 125±3℃ X5R,Y5V: 85±3℃</li><li>* To apply voltage: (1) Cap.≥0.1uF : 100% of rated voltage (2) 6.3V: 150% of rated voltage. (3) &gt;6.3V: 200% of rated voltage.</li><li>* Test time: 1000+24/-0 hrs.</li></ul> Measurement to be made after keeping at room temp. for 24±2 hrs. (Class I) or 48±4 hrs. (Class II).	<ul style="list-style-type: none"><li>* No remarkable damage.</li><li>* Cap change: NP0: within ±3.0% or ±0.3pF whichever is larger. X7R, X5R: ≥10V, within ±12.5%, 10V ≥0.1μF, within ±25%; 6.3V, within ±25% Y5V: ≥10V, within ±30% 6.3V, within +30/-40%</li><li>* Q/D.F. value: NP0: Cap≥30pF, Q≥350; 10pF≤Cap&lt;30pF, Q≥275+2.5C Cap&lt;10pF; Q≥200+10C X7R, X5R:</li></ul> <table><tr><th>Rated vol.</th><th>D.F.</th><th colspan="2">Exception of D.F.</th></tr><tr><td>≥50V</td><td>≤6%</td><td>---</td><td></td></tr><tr><td>25V</td><td>≤5%</td><td>≤10%</td><td>0201≥0.01uF</td></tr><tr><td>16V</td><td>≤5%</td><td>≤15%</td><td>0201≥0.01uF</td></tr><tr><td>10V</td><td>≤7.5%</td><td>≤20%</td><td>0201≥0.1uF</td></tr><tr><td>6.3V</td><td>≤15%</td><td>≤30%</td><td>0201≥0.1uF</td></tr></table> <ul style="list-style-type: none"><li>* I.R.: ≥10V, ≥1GΩ or RxC≥50Ω-F whichever is smaller. 6.3V; 10V:0201≥47nF, RxC≥10Ω-F</li></ul>	Rated vol.	D.F.	Exception of D.F.		≥50V	≤6%	---		25V	≤5%	≤10%	0201≥0.01uF	16V	≤5%	≤15%	0201≥0.01uF	10V	≤7.5%	≤20%	0201≥0.1uF	6.3V	≤15%	≤30%	0201≥0.1uF
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6.3V	≤15%	≤30%	0201≥0.1uF																								

## 10. APPENDIXES

### ■ Tape & reel dimensions

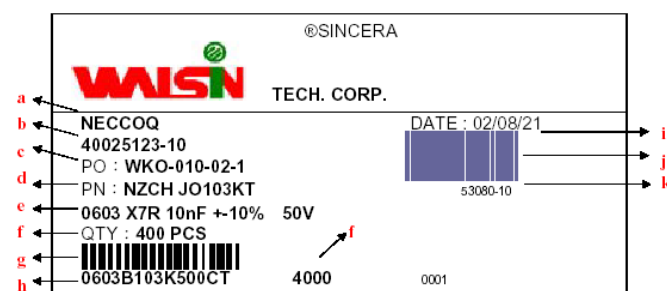


Size	0201
Thickness	L
A <sub>0</sub>	0.38±0.05
B <sub>0</sub>	0.68±0.05
T	0.42±0.05
K <sub>0</sub>	-
W	8.00±0.10
P <sub>0</sub>	4.00±0.10
10xP <sub>0</sub>	40.0±0.10
P <sub>1</sub>	2.00±0.05
P <sub>2</sub>	2.00±0.05
D <sub>0</sub>	1.55±0.05
D <sub>1</sub>	-
E	1.75±0.05
F	3.50±0.05



Size	0201	
Reel size	7"	13"
C	13.0+0.5/-0.2	13.0+0.5/-0.2
W <sub>1</sub>	8.4+1.5/-0	8.4+1.5/-0
A	178.0±0.10	330.0±1.0
N	60.0+1.0/-0	100±1.0

### ■ Description of customer label

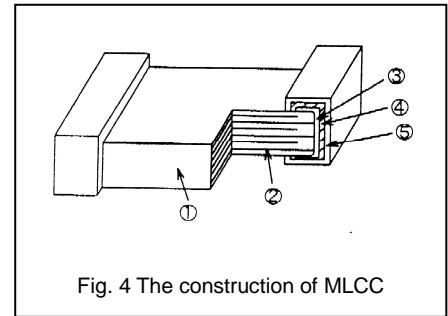


- a. Customer name
- b. WTC order series and item number
- c. Customer P/O
- d. Customer P/N
- e. Description of product
- f. Quantity
- g. Bar code including quantity & WTC P/N or customer
- h. WTC P/N
- i. Shipping date
- j. Order bar code including series and item numbers
- k. Serial number of label



## ■ Constructions

No.	Name	NP0	X7R, X5R
①	Ceramic material	BaTiO <sub>3</sub> based	
②	Inner electrode	AgPd alloy	Ni
③	Termination	Inner layer	Ag
④		Middle layer	Ni
⑤		Outer layer	Sn (Matt)



## ■ Storage and handling conditions

- (1) To store products at 5 to 40°C ambient temperature and 20 to 70% related humidity conditions.
- (2) The product is recommended to be used within one year after shipment. Check solderability in case of shelf life extension is needed.

### Cautions:

- a. The corrosive gas reacts on the terminal electrodes of capacitors, and results in the poor solderability. Do not store the capacitors in the ambience of corrosive gas (e.g., hydrogen sulfide, sulfur dioxide, chlorine, ammonia gas etc.)
- b. In corrosive atmosphere, solderability might be degraded, and silver migration might occur to cause low reliability.
- c. Due to the dewing by rapid humidity change, or the photochemical change of the terminal electrode by direct sunlight, the solderability and electrical performance may deteriorate. Do not store capacitors under direct sunlight or dewing condition. To store products on the shelf and avoid exposure to moisture.

## ■ Recommended soldering conditions

The lead-free termination MLCCs are not only to be used on SMT against lead-free solder paste, but also suitable against lead-containing solder paste. If the optimized solder joint is requested, increasing soldering time, temperature and concentration of N<sub>2</sub> within oven are recommended.

